



**UNITED STATES DEPARTMENT OF COMMERCE
Patent and Trademark Office**

Address: COMMISSIONER OF PATENTS AND TRADEMARKS
Washington, D.C. 20231

AMJ

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.
-----------------	-------------	----------------------	---------------------

08/992,222 12/17/97 HOBBS

W INPA:056

LM01/0407

WILLIAM W. KIDD
BLAKELY, SOKOLOFF
TAYLOR & ZAFMAN, LLP
12400 WILSHIRE BLVD., 7TH FLOOR
LOS ANGELES CA 90025

EXAMINER

PANCHOLI, J

ART UNIT

PAPER NUMBER

2781

DATE MAILED:

04/07/00

9

Please find below and/or attached an Office communication concerning this application or proceeding.

Commissioner of Patents and Trademarks

Office Action Summary

Application No.
08/992,222

Applicant(s)
William A. Hobbs et al

Examiner
Jigar Pancholi

Group Art Unit
2781



☒ Responsive to communication(s) filed on Jan 24, 2000

☒ This action is **FINAL**.

☐ Since this application is in condition for allowance except for formal matters, **prosecution as to the merits is closed** in accordance with the practice under *Ex parte Quayle*, 35 C.D. 11; 453 O.G. 213.

A shortened statutory period for response to this action is set to expire 3 month(s), or thirty days, whichever is longer, from the mailing date of this communication. Failure to respond within the period for response will cause the application to become abandoned. (35 U.S.C. § 133). Extensions of time may be obtained under the provisions of 37 CFR 1.136(a).

Disposition of Claim

☒ Claim(s) 1-30 is/are pending in the applicat

Of the above, claim(s) _____ is/are withdrawn from consideration

☐ Claim(s) _____ is/are allowed.

☒ Claim(s) 1-30 is/are rejected.

☐ Claim(s) _____ is/are objected to.

☐ Claims _____ are subject to restriction or election requirement.

Application Papers

☐ See the attached Notice of Draftsperson's Patent Drawing Review, PTO-948.

☐ The drawing(s) filed on _____ is/are objected to by the Examiner.

☐ The proposed drawing correction, filed on _____ is ☐ approved ☐ disapproved.

☐ The specification is objected to by the Examiner.

☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. § 119

☐ Acknowledgement is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d).

☐ All ☐ Some* ☒ None of the CERTIFIED copies of the priority documents have been

☐ received.

☐ received in Application No. (Series Code/Serial Number) _____.

☐ received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

*Certified copies not received: _____

☐ Acknowledgement is made of a claim for domestic priority under 35 U.S.C. § 119(e).

Attachment(s)

☐ Notice of References Cited, PTO-892

☐ Information Disclosure Statement(s), PTO-1449, Paper No(s). _____

☐ Interview Summary, PTO-413

☐ Notice of Draftsperson's Patent Drawing Review, PTO-948

☐ Notice of Informal Patent Application, PTO-152

— SEE OFFICE ACTION ON THE FOLLOWING PAGES —

Art Unit:

DETAILED ACTION

Response to Arguments

1. Applicant's arguments filed 1/24/00 have been fully considered but they are not persuasive. The examiner feels that Gates (5,701,409) teaches the claimed limitations. The examiner can refer back to the specifications only a limited amount, from what is stated in the claims. Therefore, the examiner feels that though the "predefined bus stimuli instruction" might be different then the error commands of Gates reference, from the claim language, the Gates reference reads on the limitations. A predefined bus stimuli instruction, in the examiner's view point, is only a known instruction that causes an action on the bus. The error command the Gates discusses is a known instruction that causes an action on the bus. Therefore the rejection will be maintained.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless --

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371© of this title before the invention thereof by the applicant for patent.

Art Unit:

3. Claims 1, 2, 4-6 and 9-30 are rejected under 35 U.S.C. 102(e) as being anticipated by Gates (5,701,409).
 - a. As for claims 1, 2, 16, 21, 22 and 25-30, Gates teaches a system to test a bus comprising at least one instruction memory to store a predefined bus stimuli instruction and at least one phase generator coupled between the bus and the instruction memory for providing signals to the bus in response to the predefined bus stimuli instruction (abstract).
 - b. As for claim 4, it is inherently seen that the IC (phase generator) can also receive signals from the bus.
 - c. As for claims 5, 17 and 23, it is inherently seen that some type of storing of predefined responses must be included for the device to compare results to detect errors.
 - d. As for claims 6 and 18, Gates teaches the phase generator includes one digital logic device responsive to the instructions and one phase engine for controlling timing (abstract; col. 2, lines 40-45).
 - e. As for claims 9 and 19, Gates teaches a control portion and data portion (col. 5, lines 27-45).
 - f. As for claim 10, Gates inherently teaches the control portion includes a flow logic device.

Art Unit:

- g. As for claim 11, Gates inherently teaches the phase engine includes at least on logic level translation device.
- h. As for claim 12, it is inherently seen that these phases are included since a PCI bus includes these phases.
- i. As for claims 13, 14, 20 and 24, Gates teaches a data memory coupled to the data portion and that the data portion receives data from the bus (col. 5, lines 27-45).
- j. As for claim 15, Gates teaches a system to test a bus comprising at least one instruction memory to store a predefined bus stimuli instruction and at least one phase generator coupled between the bus and the instruction memory for providing signals to the bus in response to the predefined bus stimuli instruction (abstract).
It is inherently seen that the IC (phase generator) can also receive signals from the bus. It is inherently seen that some type of storing of predefined responses must be included for the device to compare results to detect errors. Gates teaches the phase generator includes one digital logic device responsive to the instructions and one phase engine for controlling timing (abstract; col. 2, lines 40-45). Gates teaches a control portion and data portion (col. 5, lines 27-45). Gates inherently teaches the control portion includes a flow logic device. Gates inherently teaches the phase engine includes at least on logic level translation device. Gates teaches a data memory coupled to the data portion and that the data portion receives data from the bus (col. 5, lines 27-45).

Art Unit:

Claim Rejections - 35 USC § 103

4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

5. Claims 3, 7 and 8 are rejected under 35 U.S.C. 103(a) as being unpatentable over Gates (5,701,409).

- a. As for claim 3, Gates inherently teaches the instruction comprises an instruction word (col. 2, lines 40-45). Gates doesn't teach that the instruction word has a predefined length. Official Notice is taken that instruction words of predefined lengths are well known in the art. It would have been obvious to a person of ordinary skill in the art, at the time of the invention, to have used instruction words of any length because they are only a matter of computer design.
- b. As for claims 7 and 8, Gates doesn't teach what the digital logic device comprises. Official Notice is taken that FPGA's and ASIC's are well known in the art. It would have been obvious to a person of ordinary skill in the art, at the time of the invention, to have used instruction words of any length because they are only a matter of computer design.

Art Unit:

Conclusion

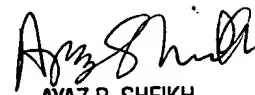
6. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jigar Pancholi whose telephone number is (703) 306-3012. The examiner can normally be reached on Monday through Thursday from 7:00 to 4:30. The examiner can also be reached on alternate Fridays. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Ayaz Sheikh, can be reached on (703) 305-9648. The fax phone number for Formal or Official faxes for this Group is (703) 308-9051 or (703) 308-9052. The fax phone number of informal or drafts is (703) 305-3718. Communications via Internet e-mail regarding this application, other than those under 35 U.S.C. 132 or which otherwise require a signature, may be used by the

Art Unit:

applicant and should be addressed to [ayaz.sheikh@uspto.gov]. All Internet e-mail communications will be made of record in the application file. PTO employees do not engage in Internet communications where there exists a possibility that sensitive information could be identified or exchanged unless the record includes a properly signed express waiver of the confidentiality requirements of 35 U.S.C. 122. This is more clearly set forth in the Interim Internet Usage Policy published in the Official Gazette of the Patent and Trademark on February 25, 1997 at 1195 OG 89. Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (703) 305-3900.



AYAZ R. SHEIKH
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2700



JKP

4/3/2000